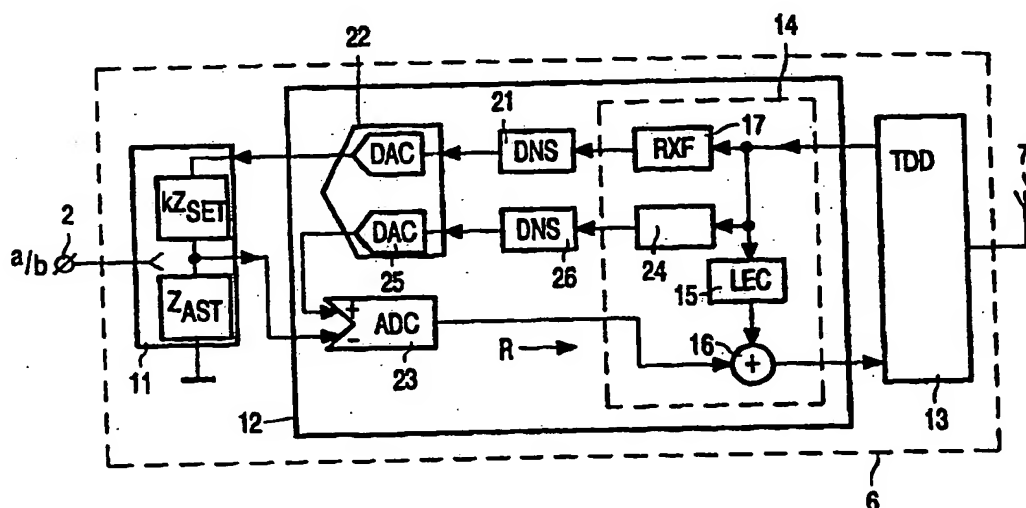




## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(54) Title: **A METHOD FOR CANCELLING ECHOES IN A COMMUNICATION SYSTEM, A COMMUNICATION DEVICE, AND A RADIO BASE STATION OF A CORDLESS TELEPHONE DEVICE**



## (57) Abstract

The invention provides a method and a communication device for cancelling echoes in the communication device, comprising: receiving path means and sending path means for receiving and sending respective signals, and echo compensating means for generating an echo control signal which is representative of a perceived echo, which echo compensating means are coupled between the sending and the receiving path means, and comprise an echo digital control signal generating means, and a digital-to-analog convertor coupled to the digital control signal generating means and to the receiving path means. Hardware and software implementation are easy and the dynamic range of an analog-to-digital convertor present in the receiving path is limited.

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"A method for cancelling echoes in a communication system, a communication device, and a radio base station of a cordless telephone device"

The present invention relates to a method for cancelling echoes in a communication system, as outlined in the first part of claim 1.

The present invention also relates to a communication device and to a radio base station of a cordless telephone device comprising such a communication device, as  
5 outlined in the first parts of claim 2 and 10, respectively.

Such a method and communication device, for example, for a radio base station of a cordless telephone device, is applied in and known from Philips Integrated  
10 Circuits Data Sheet, entitled "PCA 1070, Multistandard Programmable Analog CMOS Transmission IC", specifying the PCA 1070 line interface features, and suitable for performing speech and line signal interface functions in electronic telephone sets. In particular the known communication device comprises an analog local-echo-cancelling means, generally also referred to as antisidetone (AST) means, for cancelling local echoes  
15 originating from its own local interface, mainly from the so-called hybrid circuit, which performs the separation and combination of respective signals in the sending and receiving paths, respectively, in order to couple a 4-wire telephone interface to the well known 2-wire a/b terminals of a telephone line.

In practise, the aforementioned interface is combined with a digital  
20 interface part wherein aside from analog local-echo-cancelling also digital local-echo-cancelling (LEC) is performed for cancelling echoes originating from the nearby interface itself. There is a strong tendency to cancel all kinds of echoes, irrespective of their source, to which the human ear is very sensitive and which may cause irritation and misunderstanding between parties trying to communicate with each other. In particular,  
25 echoes are more perceptible and thus more disturbing in present-day high-quality digital European cordless telephone (DECT) systems, due to the roundtrip delay of 20 msec.

It is an object of the present invention to provide an alternative method

and a communication device by means of which echoes can be cancelled in an easy-to-implement way and in a way which allows flexible and programmable adjustments to a variety of national authority communication line requirements.

To achieve this, the present invention has the features outlined in claim 1.

5           Although the proposed invention seems more complicated because of the inclusion of a digital-to-analog convertor (DAC) apart from the generally already present further A/D and D/A convertors in the receiving and sending paths, respectively, it has surprisingly been found that taking an echo representative control signal from the digital sending path, followed by the digital-to-analog conversion and the addition of the converted  
10 control-compensation signal to the receiving path, obviates the above disadvantages without adding substantial complexity to the above-mentioned digital software implemented signal processing and programming.

In order to achieve the above objective, the communication device according to the invention has the characterising features outlined in claim 2.

15           In addition to the above advantages, the communication device according to the invention gives rise to a possible limitation of the dynamic range of an ADC which is generally also present in the receiving path of the communication device. This dynamic range is a key factor of such a convertor, and an increase thereof would add to the complexity, required chip area and current consumption in an interface IC for communication purposes.  
20 The thus achieved limited dynamic range of said ADC provides opportunities for supplying a lower supply voltage to the communication device, which supply voltage is known to limit the dynamic range at its high end in case of a remote PABX, because it is taken from the telephone line itself. At its lower end the dynamic range is limited by the lowest voltage which does not drown in noise. With the limited dynamic range of the ADC, noise  
25 requirements are thus advantageously weakened without jeopardising the overall performance of the communication device according to the invention.

A further embodiment of the communication device according to the invention having the features outlined in one of the claims 3, 4 or 5 has the advantage that the proposed inclusion or combination of echo digital control signal generating means and/or  
30 receiving filter means in the digital signal processing (DSP) means only causes a small and therefore acceptable increase of the DSP software code and execution time, as well as a minor increase - only necessitating a doubled output register - in DSP hardware.

In the advantageous embodiments of the communication device according to the invention outlined in claims 6 or 7 only one symmetrical digital-to-analog convertor

(DAC) is used. This however does not cause a substantial increase in hardware because, for reasons of performance, a symmetrical DAC is preferred anyhow, because of its reduced output noise. Thus, in practise this does not lead to chip area increase or additional current consumption.

5                   The inclusion of a dynamic noise shape (DNS) circuit as proposed in claim 8 only results in a very limited and thus acceptable increase of the chip area in the order of less than 0.05 mm<sup>2</sup>.

10                   The method, communication device and radio base station according to the invention will be elucidated further together with the additional advantages, while reference is made to the appended drawing, wherein similar components are referred to by the same reference numerals. In the drawing:

Fig. 1 shows possible arrangements of prior art communication devices,

15                   Fig. 2 shows a possible embodiment of the communication device according to the invention,

Fig. 3 shows a possible arrangement of key impedances simulated in the communication device according to the invention, and

20                   Figs. 4A and 4B show possible and optimised connection schemes respectively for incorporating the simulated key impedances in the communication device according to the invention.

Fig. 1 shows a communication device 1, such as a telephone, which is  
25                   connected to a communication line 2, such as the well known a/b telephone line. The communication device 1 usually has a low frequency data source, for example a microphone 3, facsimile or the like, and a telephone or loudspeaker 4. Data is thus transferred between the communication line 2 and the communication device 1. The communication device 1 may be mobile, portable or cordless. In the case of for example a cordless communication device  
30                   1, the device has a portable part 5 and a base station 6, mutually communicating through antennas 7 and 8. In general the microphone 3 and loudspeaker 4 are connected to the communication line 2 through a line interface 10. The integrated circuit PCA 1070 mentioned earlier contains circuitry like interface 10 and in particular an analog echo cancelling means, also called analog antisidetone circuit AST, for cancelling echoes caused

by a hybrid 11 included in the interface 10.

Fig. 2 shows an embodiment of a part of the communication device 1, in particular the part included in the base station 6 is exemplified. The base station 6 comprises the line interface 10 containing the hybrid 11 and several separate discrete electrical components, a usually on-chip circuit 12 connected to the interface 10, and a time division duplex (TDD) circuit 13 connected between the circuit 12 and the antenna 7. The portable part 5 also contains a TDD circuit 9 meant for sending signals to and receiving signals from the base station 6. Viewed schematically, the communication device 1 has a sending path S from microphone 3 to at least a/b line 2, and a receiving path R from at least a/b line 2 to loudspeaker 4. This path distinction can also be made in circuit 12. The circuit 12 contains a digital signal processing circuit, or DSP 14 for short. The DSP 14 usually comprises a digital local echo canceller (LEC) 15, which like the analog AST mentioned above is coupled between the sending path S and the receiving path R. The DSP 14 digitally cancels or compensates echoes originating from local hybrids present in the communication device 1. The hybrid calls for echo compensation, cancellation or antisidetone technics. These technics develop a control signal or echo compensation signal from the sending path S, which is representative of an echo experienced in the communication device 1. Such a control signal is then inverted and added to the signal in the receiving path R in order to compensate for the echo and supply a return signal to the loudspeaker 4 which is virtually free of the echo compensated for. Local echoes originating from the communication device 1 itself and arising on a moderate timescale of approximately up to 5 msec are compensated for either in an analog way in the (AST) line interface 11 or digitally in the LEC 15. For this purpose, transceiver 13 outputs a digital signal in the S-path to DSP 14 and receives a digital signal sent by a communication device at the other end of the line 2 through DSP 14. DSP 14 comprises the LEC 15, which generates a digital representation of an echo and adds it, inverted in an adder 16, to the digital return signal in the R-path. DSP 14 further comprises a receiving filter means (RXF) 17 containing as shown in Fig. 4, a series arrangement of a highpass filter 18, digital filter means 19 and a lowpass filter 20 for filtering out the unwanted spectral lobes of the output signal. The implementation and functioning of the receiving filter means 17 is known per se in the relevant art. The filter means output signal is then fed to a digital noise shape (DNS) circuit 21 and digital-to-analog converted in DAC means 22 so as to be provided to line interface 11. The signal in the R-path from interface 11 is fed to a minus input and then analog-to-digital converted in ADC 23, which in turn is connected to adder 16.

Alternative echo compensation is provided for in DSP 14 by digital signal means 24 for generating a digital echo signal, which is coupled to a second DAC 25, included in the DAC means 22. Optionally a second digital noise shape circuit 26 is connected between the means 24 and DAC 25. DAC 25 has an output 27 connected to a plus input of ADC 23. The digital signal means 24 is designed and arranged to generate an echo signal to compensate for an echo originating from the communication device at the other end of the a/b line 2.

Because ADC input signals are input into plus and minus inputs of the ADC 23 its dynamic range is limited. The merits thereof are such that they largely offset the disadvantages of the presence of the second DAC 25 and its optional DNS circuit 26. However since DAC means 22 are symmetrical, the second DAC 25 is given for free, as it is implicitly present anyhow. The optional DNS 26 on the other hand costs only a very limited chip area in practise.

In addition, the receiving filter means 17 and the echo digital control signal generating means 24 can be combined to save memory space both for programming and data handling in the digital signal processing means 14. Further advantages can be achieved as follows. The communication device 1 contains key impedances which are vital to match the internal impedance of the communication device 1 for each and every country with the locally present line impedance and to match an internal echo cancelling impedance with an impedance reflecting the echo properties at the specific location of the communication device 1. These impedances ( $kZ_{SET}$ ,  $Z_{AST}$ ), that are all variable (resistors  $R1...R4$ , and capacitors  $C1$ ,  $C2$ ) in order to be able to adjust their values to the above mentioned requirements, are schematically shown in fig. 3. Given the notion of the present invention, inclusion of these key impedances by simulating them in the DSP 14 is proposed. Digital simulation provides on-chip flexibility and programmability of the key impedances as well as easy on-chip integration. Given a transfer function  $H(s)$  in terms of the Laplace operator of the circuit of fig. 3, this transfer function can easily be transformed by bi-linear mapping of  $H(s)$  into the well known z-domain. This yields  $H(z)$  in terms of the z-operator, which is realised by adding EQ1 after EQ to the digital filter circuit of fig. 4A. By properly identifying the variable coefficients in the second order digital filter function with the resistance and capacitor values, digital simulation thereof is achieved. This solution is easier to realise than building the variable programmable key impedances on-chip, for example by means of switched capacitor circuits.

Fig. 4B shows a combined connection scheme for the RXF 17 and the

digital signal means 24, where EQ1 is divided out. This saves program memory space and reduces the number of instructions to be executed in DSP 14.

In principle, means (not shown) can now be provided for automatically measuring local electrical subscriber line conditions and characteristics on a/b line 2 for  
5 adapting the simulated key impedances such that they automatically reflect an optimum matching to line impedance and/or provide optimum echo cancelling on the spot. These conditions are line voltage, line current, impedance behaviour as a function of frequency, echo impulse response etcetera. These conditions thus provide data for automatically calculating the optimum values for the simulated components, schematically shown in fig. 3.

10 In view of the foregoing it will be evident to a person skilled in the art that various modifications may be made within the spirit and the scope of the present invention as hereinafter defined by the appended claims and that the present invention is thus not limited to the examples provided.



## CLAIMS:

1. A method for cancelling echoes in a communication system, wherein respective signals are sent through a sending path and received through a receiving path, which method comprises the steps of supplying to the receiving path an echo control signal which is derived from the sending path and is representative of an echo to be cancelled in the  
5 receiving path, characterised in that the echo-control signal is an echo digital control signal which is digital-to-analog converted and then supplied to the receiving path.
2. A communication device for cancelling echoes in the communication device, comprising:  
10 - receiving path means and sending path means for receiving and sending respective signals; and  
- echo compensating means for generating an echo control signal which is representative of a perceived echo, which echo compensating means are coupled to the sending path means and the receiving path means, characterised in that the echo compen-  
15 sating means comprise an echo digital control signal generating means, and a digital-to-analog convertor coupled to the digital control signal generating means and to the receiving path means for compensating echoes.
3. The communication device according to claim 2, characterised in that the  
20 communication device comprises a digital signal processing means incorporating a digitally implemented part of the receiving path means and/or the sending path means, and in that the echo digital control signal generating means is also incorporated in the digital signal processing means.
- 25 4. The communication device according to claim 3, characterised in that the digital signal processing means includes a receiving filter means.
5. The communication device according to claim 4, characterised in that the receiving filter means and the echo digital control signal generating means are combined in

the digital signal processing means.

6. The communication device according to claim 5, characterised in that the communication device comprises an arrangement of two key impedances, one key impedance being representative of an internal impedance of the communication device and the other being representative of echo capabilities, which key impedances are digitally simulated in the digital signal processing means.

7. The communication device according to claim 6, characterised in that the communication device comprises means for automatically measuring local electrical subscriber line conditions and characteristics for adapting said values such that they automatically reflect an optimum matching to line impedance and/or provide optimum echo cancelling.

8. The communication device according to one of the claims 2-7, characterised in that the communication device comprises a further digital-to-analog convertor which is combined with the digital-to-analog convertor which is coupled to the echo digital control signal generating means to form a single symmetrical digital-to-analog convertor.

9. The communication device according to one of the claims 2-8, characterised in that the communication device comprises at least one digital noise shape circuit coupled between the echo digital control signal generating means and/or the receiving filter means on the one hand and the at least one digital-to-analog convertor on the other hand.

10. A radio base station of a cordless telephone device comprising a communication device according to one of the claims 2-9, for cancelling echoes in the communication device, comprising:

- receiving path means and sending path means for the receiving and sending of respective signals; and
- echo compensating means for generating an echo control signal which is representative of a perceived echo, which echo compensating means are coupled to the receiving path means, characterised in that the echo compensating means comprise an echo digital control signal generating means and a digital-to-analog convertor coupled to the digital control signal generating means and to the receiving path means for compensating echoes.

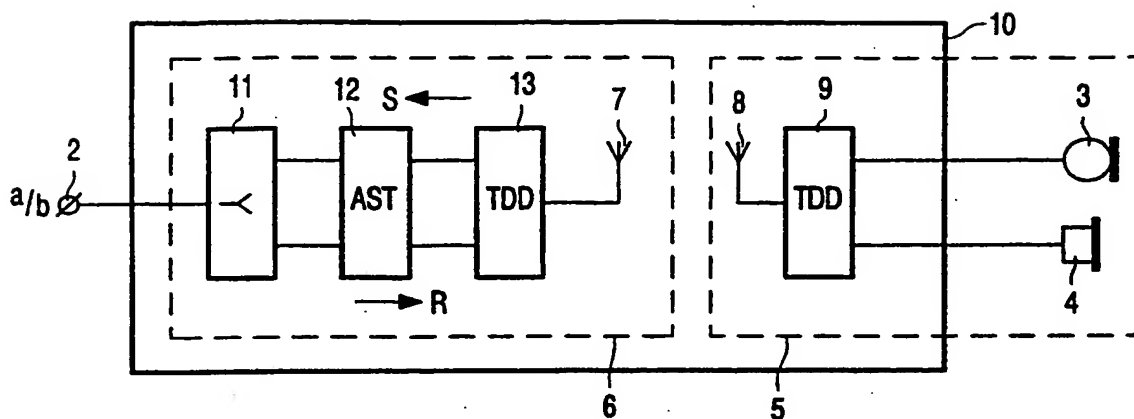
$\frac{1}{2}$ 

FIG. 1

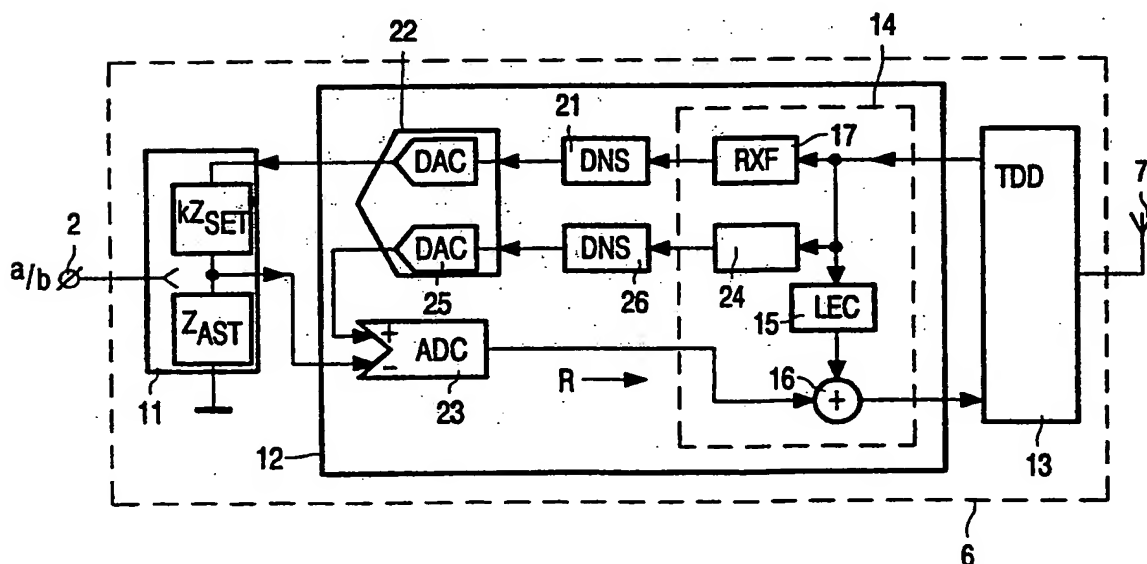


FIG. 2

2/2

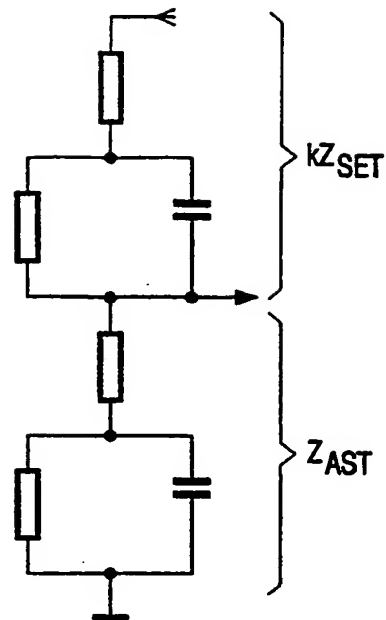


FIG. 3

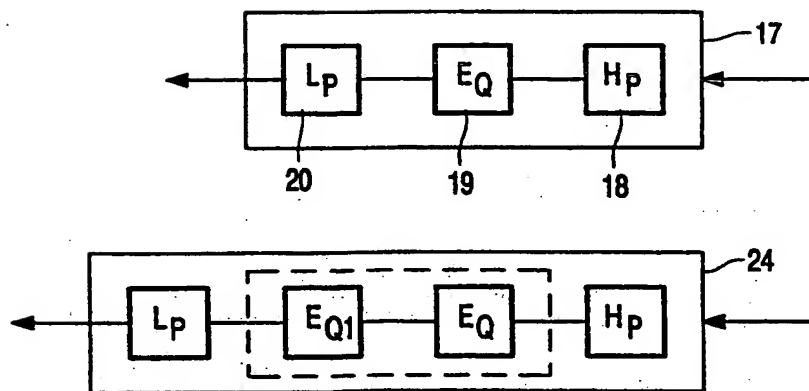


FIG. 4A

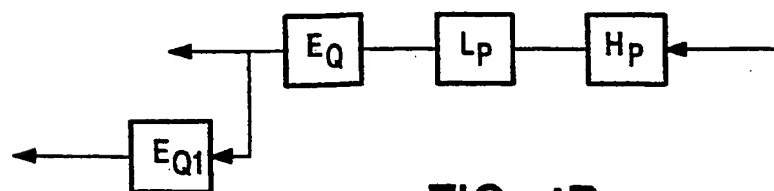


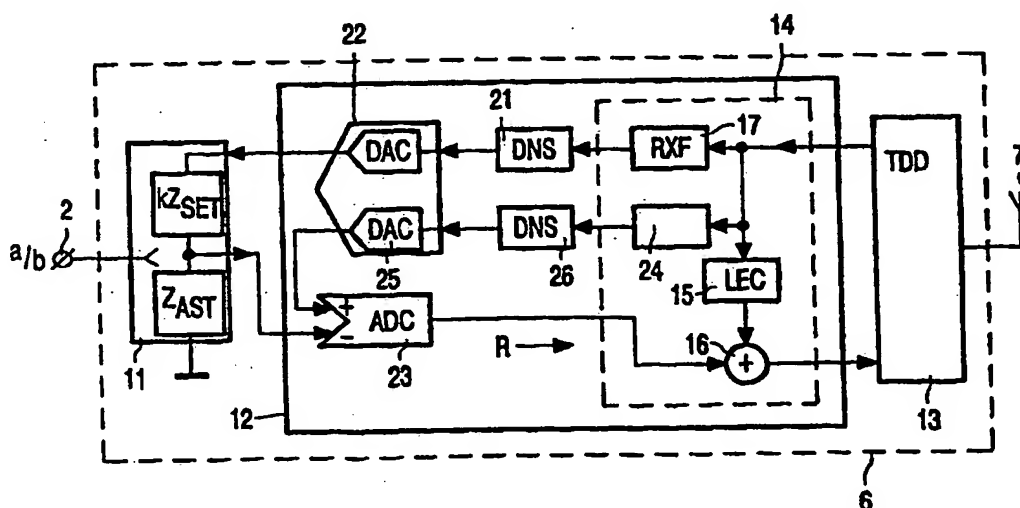
FIG. 4B



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(21) International Application Number: PCT/IB99/00058 (22) International Filing Date: 18 January 1999 (18.01.99) (30) Priority Data: 98200276.8      30 January 1998 (30.01.98)      EP (71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). (71) Applicant (for SE only): PHILIPS AB [SE/SE]; Kottbygatan 7, Kista, S-164 85 Stockholm (SE). (72) Inventor: DAANEN, Antonius, M., J.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). (74) Agent: DEGUELLE, Wilhelmus, H., G.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).			(81) Designated States: CN, JP, KR, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.          Before the expiration of the time limit for amending the claims          and to be republished in the event of the receipt of amendments.</i> (88) Date of publication of the international search report: 21 October 1999 (21.10.99)

(54) Title: A METHOD FOR CANCELLING ECHOES IN A COMMUNICATION SYSTEM, A COMMUNICATION DEVICE, AND A RADIO BASE STATION OF A CORDLESS TELEPHONE DEVICE



## (57) Abstract

The invention provides a method and a communication device for cancelling echoes in the communication device, comprising: receiving path means and sending path means for receiving and sending respective signals, and echo compensating means for generating an echo control signal which is representative of a perceived echo, which echo compensating means are coupled between the sending and the receiving path means, and comprise an echo digital control signal generating means, and a digital-to-analog convertor coupled to the digital control signal generating means and to the receiving path means. Hardware and software implementation are easy and the dynamic range of an analog-to-digital convertor present in the receiving path is limited.

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# INTERNATIONAL SEARCH REPORT

International application No.

PCT/IB 99/00058

## A. CLASSIFICATION OF SUBJECT MATTER

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WPI, JAPIO

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 4355214 A (MICHEL LEVY ET AL), 19 October 1982 (19.10.82), column 10, line 28 - column 11, line 49, figure 3	1-10
A	EP 0727882 A2 (ADVANCED MICRO DEVICES INC.), 21 August 1996 (21.08.96), see the whole document	1-10
A	US 5428681 A (TORE M. ANDRE), 27 June 1995 (27.06.95), see the whole document	1-10

☐ Further documents are listed in the continuation of Box C.

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Information on patent family members

02/08/99

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 4355214 A	19/10/82	BE 883625 A CA 1149979 A DE 3022553 A,C FR 2460075 A,B GB 2052928 A,B IE 49826 B IT 1128890 B LU 82534 A NL 8003589 A	04/12/80 12/07/83 08/01/81 16/01/81 28/01/81 25/12/85 04/06/86 03/02/81 24/12/80
EP 0727882 A2	21/08/96	JP 8331021 A US 5852661 A	13/12/96 22/12/98
US 5428681 A	27/06/95	AU 665231 B AU 3411793 A BR 9207028 A CA 2125658 A EP 0620956 A FI 943271 A JP 7503109 T MX 9300074 A NO 942418 A SE 469677 B,C SE 9200062 A US 5443890 A WO 9314566 A	21/12/95 03/08/93 05/12/95 22/07/93 26/10/94 08/07/94 30/03/95 01/07/93 24/06/94 16/08/93 11/07/93 22/08/95 22/07/93